

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

Germany

2,523,853	K. Heuber, R. Remshardt, K. Wajmann, W. Klein and S. K. Wiedmann	Method and Circuit Arrangement for Operating a Highly Integrated Monolithic Information Store
2,537,162	A. Schmackpfeffer and W. Jaerisch	Method for Measuring Contact Dissolution of Surfaces

Switzerland

586,983	H. R. Schindler, P. Vettiger and H. R. Mueller	A Subscriber Line Interface Circuit
586,984	A. H. Frei, H. R. Mueller, H. R. Schindler and P. Vettiger	A Subscriber Line Interface Circuit

United Kingdom

975,695	N. M. Leon	Printing Machine for Tickets
---------	------------	------------------------------

United States

4,001,570	D. N. Gooding and E. M. Shimp	Arithmetic Unit for a Digital Data Processor
4,002,511	I. E. Magdo and S. Magdo	Method for Forming Masks Comprising Silicon Nitride and Novel Mask Structures Produced Thereby
4,002,946	R. C.-C. Liang	Latching Driver Circuit for a Gas Panel Display
4,003,001	G. D. Jones, Jr.	Multi-Line, Multi-Mode Modulator Using Bandwidth Reduction for Digital FSK and DPSK Modulation
4,003,023	G. G. Benson and E. R. Jones	Post-Recognition Segmentation For Pattern-Recognition Machines
4,003,025	J. J. Hilliard, P. J. Mullan and W. S. Rosenbaum	Alphabetic Character Word Upper/Lower Case Print Convention Apparatus and Method
4,003,037	H. Chang	Magnetic Bubble Domain Processing Apparatus and Method
4,003,091	G. T. Wright	Transducer Support and Stabilizer
4,003,569	D. R. Andrews and R. J. Leclere	Last Copy Detection
4,003,768	R. L. Anderson, E. E. Castellani, P. M. McCaffrey and L. T. Romankiw	Method for Treating Magnetic Alloy to Increase the Magnetic Permeability
4,004,043	H. Hiraoka	Nitrated Polymers as Positive Resists
4,004,044	J. R. Franco, J. Havas and L. J. Rompala	Method for Forming Patterned Films Utilizing a Transparent Lift-Off Mask
4,004,164	H. C. Cranford, Jr. and C. R. Hoffman	Compensating Current Source
4,004,170	J. D. Henke	MOSFET Latching Driver
4,004,275	R. L. Arndt and T. R. Teal	Self-Clocking Data Entry Unit System
4,004,449	E. F. Gorey and G. H. Schwuttke	Impact Sound Stressing for Semiconductors
4,004,795	M. C. Agnew, J. J. Best and W. L. Gudgel	Hopper Mechanism
4,004,849	M. D. Shattuck	Display Apparatus and Process
4,004,936	J. L. Powell	Low Temperature Sealed Glass Compositions and Process for Their Preparation
4,005,276	P. Abramson and U. Appel	Digital Voice Signaling with Digital Echo Detection and Voice Activity Compression Used to Cancel Echo
4,005,390	G. I. Findley	Merger and Multiple Translate Tables in a Buffered Printer
4,005,411	T. H. Morrin, II	Compression of Gray Scale Imagery to Less than One Bit Per Picture Element
4,005,469	A. W.-C. Chang and V. J. Lucarini	P-Type-Epitaxial-Base Transistor with Base-Collector Schottky Diode Clamp
4,005,471	I. E. Magdo and S. Magdo	Semiconductor Resistor Having a High Value Resistance for Use in an Integrated Circuit Semiconductor Device
4,005,477	H. H. Ottesen	Phase Equalized Readback Apparatus and Methods
4,005,482	F. Castro and R. L. O'Day	Asymmetric Erase Across a Rotary Transformer to a Rotating Magnetic Head

4,005,698	J. J. Cuomo, J. M. Woodall and J. F. Ziegler	Photon Energy Converter
4,006,045	J. A. Aboaf, R. W. Broadie, E. M. Hull and H. B. Pogge	Method for Producing High Power Semiconductor Device Using Anodic Treatment and Enhanced Diffusion
4,006,365	C. R. Marzin, C. M. Rougeaux and P. J. C. Vernes	Exclusive or Integrated Logic Circuits Using Complementary MOSFET Technology
4,006,433	A. F. Higginbotham and K. L. Jeffries	Bias/Erase Oscillator
4,006,456	J. M. Wilk	Loop Fault Location and Isolation
4,006,465	J. L. Cross, M. E. Homan, G. K. Machol, R. L. V. Malm and L. E. Svelund	Apparatus for Control and Data Transfer Between a Serial Data Transmission Medium and a Plurality of Devices
4,006,469	G. W. Leehan, S. F. Minter, III and A. J. Sassa	Data Storage Cell with Transistors Operating at Different Threshold Voltages
4,006,492	E. B. Eichelberger and G. J. Robbins	High Density Semiconductor Chip Organization
4,007,047	L. H. Kaplan and S. M. Zimmerman	Modified Processing of Positive Photoresists
4,007,103	T. H. Baker, M. Ghafghaichi, R. C. Stevens and H. Wimpfheimer	Planarizing Insulative Layers by Resputtering
4,007,442	G. I. Findley and T. L. Anderson	Intermixed Line Heights and Blank Line Formation in a Buffered Printer
4,007,445	G. E. Keefe and Y. S. Lin	Minimum Structure Bubble Domain Propagation
4,007,450	L. H. Haibt and A. P. Mullery	Data Sharing Computer Network
4,007,451	K. Heuber, W. Klein, K. Najmann, R. Remshardt and S. K. Wiedmann	Method and Circuit Arrangement for Operating a Highly Integrated Monolithic Information Store
4,007,464	E. Bassous, L. Kuhn, A. Reisman and H. H. Taub	Ink Jet Nozzle
4,007,465	K. C. Chaudhary	System for Self-Cleaning Ink Jet Head
4,007,871	A. L. Jones and R. M. Kellogg	Centrifuge Fluid Container
4,007,989	E. S. Wajda	Hadamard Filter Design
4,008,111	R. F. Rutz	AlN Masking for Selective Etching of Sapphire
4,008,460	L. B. Bryant, R. J. Pedersen and A. Weinberger	Circuit for Implementing a Modified LRU Replacement Algorithm for a Cache
4,008,463	M. H. Kryder	Integrated Bubble Nucleator
4,008,491	J. J. Lynott and E. H. Storm	Fixed Head, Direct Access Storage Device
4,009,033	P. Bakos and J. Rasile	High Speed Positive Photoresist Composition
4,009,332	D. A. Van Hook	Memory Management System for an Ink Jet Copier
4,009,338	H. Dym, P. Lowy and G. Salyer	Tablet with Improved Pen Height Compensation
4,009,343	H. G. Markey and L. P. West	Switching and Activity Compression Between Telephone Lines and Digital Communication Channels
4,009,344	D. C. Flemming	Inter-Related Switching, Activity Compression and Demand Assignment
4,009,345	D. C. Flemming and H. G. Markey	External Management of Satellite Linked Exchange Network
4,009,346	B. E. Parker and L. P. West	Distributional Activity Compression
4,009,347	D. C. Flemming, H. G. Markey, R. J. Metz and L. P. West	Modular Branch Exchange and Nodal Access Units for Multiple Access Systems
4,009,353	R. L. James and J. T. Padden	Analog Signal Detection
4,009,469	P. E. Boudreau and B. B. Moore	Loop Communications System With Method and Apparatus For Switch to Secondary Loop
4,009,472	J. W. Jones	Dynamic Associative Cell
4,009,877	J. W. Ward, Jr.	Document Transport Device
4,010,064	W. J. Patrick, S. J. Scilla and W. A. Westdorp	Controlling the Oxygen Content of Czochralski Process of Silicon Crystals by Sandblasting Silica Vessel
4,010,453	S. C. Lewis	Stored Charge Differential Sense Amplifier
4,010,482	S. A. Abbas, N. G. Anantha and R. C. Dockerty	Non-Volatile Schottky Barrier Diode Memory Cell
4,010,574	L. B. Feierabend and O. R. Luhrs	Apparatus for Contouring Magnetic Head Surfaces and Method Therefor
4,010,835	B. R. Martin and J. H. Meier	Matrix Print Head
4,010,894	R. M. Kellogg and V. R. Kruger	Centrifuge Fluid Container
4,011,060	M. Berkenblit, S. A. Chan, J. B. Lander mann, A. Reisman and T. Takamori	Method of Controlling the Softening Point of Solder Glass
4,011,157	W. B. Pennebaker, Jr., K. S. Pennington, H. K. Seitz and F. Hochberg	Ultrasonic Removal of Solid Impurities from Recirculating Ink
4,011,351	E. Gipstein, W. M. Moreau and O. U. Need, III	Preparation of Resist Image with Methacrylate Polymers
4,011,449	W.-C. Ko and E. Sawatzky	Apparatus for Measuring the Beam Current of Charged Particle Beam
4,011,461	H. Chang, T. C. Chen and S.-Y. Lee	Pipelined Universal Bubble Logic Array

4,011,547	M. J. Kimmel	Data Processor for Pattern Recognition and the Like
4,011,587	N. K. Arter, T. F. Eichhorn and C. H. Kalthoff	Rotary Head Magnetic Recorder for Use with a Specific Record Member
4,011,590	A. W. Orlando	Internal Division Actuation of Ventilated Floppy Disk Stack
4,011,591	A. W. Orlando and J. Q. Weidenhammer	Internal Division Actuation of Ventilated Floppy Disk Stack
4,012,032	J. C. Rogers	Document Reproduction Having Output Means with Plural Outputs Operable in a Plurality of Modes
4,012,036	G. L. Sokol	Document Hopper
4,012,642	P. L. Gueret	Josephson Junction Logic Element
4,012,646	F. F. Fang and D. J. Herrell	Powering Scheme for Josephson Logic Circuits Which Eliminates Disturb Signals
4,012,756	P. Chaudhari, F. M. d'Heurle and A. Gangulee	Method of Inhibiting Hillock Formation in Films and Film Thereby and Multilayer Structure Therewith
4,012,781	C. C.-H. Lin	Magnetoresistive Read Head Assembly for Servo Operation
4,013,039	F. Kubilius, G. F. Pavlovic and J. Woolf	Wet Processing PH Control
4,013,485	T.-P. Ma and W. H.-L. Ma	Process for Eliminating Undesirable Charge Centers in MIS Devices
4,013,891	W.-C. Ko and E. Sawatzky	Method for Varying the Diameter of a Beam of Charged Particles
4,014,005	J. L. Fox and E. E. Marquardt	Configuration and Control Unit for a Heterogeneous Multi-System
4,014,029	R. Lane and H. H. Taub	Staggered Nozzle Array
4,014,036	I. T. Ho and H. N. Yu	Single-Electrode Charge-Coupled Random Access Memory Cell
4,014,141	S. T. Riddle and G. G. Vair	Apparatus and Method for Controlling Magnetic Head Surface Formation
4,014,576	W. O. Druschel, B. G. Gardineer, Jr., S. A. Manning and B. Musits	Article Carrier
4,015,147	E. E. Davidson and R. D. Lane	Low Power Transmission Line Terminator
4,015,203	R. L. Verkuil	Contactless LSI Junction Leakage Testing Method
4,015,249	H. L. Hu and L. L. Rosier	Transfer of Domains Between Fields
4,015,291	P. A. Stevenson and S. M. Vogel	Flying Head Helical Scan Magnetic Recording Device Employing a Lobed Rotor
4,015,615	J.-O. C. Weber and C. Yakubowski	Fluid Application System
4,015,799	J. A. Koski, R. W. Lissner and S. D. Roberts	Adaptive Reel-to-Reel Tape Control System
4,015,986	G. Paal and J. F. Wustenhagen	Method of Developing and Stripping Positive Photoresist
4,016,017	J. A. Aboaf, R. W. Broadie and W. A. Pliskin	Integrated Circuit Isolation Structure and Method for Producing the Isolation Structure
4,016,062	R. R. Mehta and O. Voegeli	Method of Forming a Serrated Surface Topography
4,016,408	G. W. Koetzle	Method and Arrangement for Supervising Power Supply Systems
4,016,431	R. A. Henle and I. T. Ho	Optimal Driver for LSI
4,016,434	R. J. DeFilippi	Load Gate Compensator Circuit
4,016,479	G. A. Reible, Jr. and G. D. Seigny	High Frequency Power Converter Drive Circuit
4,016,482	J. R. Cielo and J. A. Orfitelli	Pulse Energy Suppression Network
4,016,554	P. J. Evans and C. Williams	Raster Display Apparatus
4,016,587	F. H. De La Moneda	Raised Source and Drain IGFET Device and Method
4,016,596	I. E. Magdo and S. Magdo	High Performance Integrated Bipolar and Complementary Field Effect Transistors
4,016,603	H. H. Ottesen	Disk Storage Apparatus Having Signals Recorded in a Specific Format